

## **ABSTRACT**

The present invention relates to a method for testing a chip, particularly to a method for testing chip configuration settings, essentially installing the chip on a main board after the chip fabrication is finished. The test comprises starting power first of all, a power on self test being performed by the system; loading a BIOS program, wherein the BIOS program includes a configuration test process; testing the configuration settings of the chip by the configuration test process; inputting test data in turn; then enabling registers corresponding to the chip configuration space depending on the test data, for starting the chip operation; obtaining the data, produced by the chip operation, to be compared with an expected result, in order for performing the verification of chip configuration settings at the final stage before the actual chip operation is started, so as to speed the development and modification for the chip.